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Complete if Known

Sheet	1	of	1	Application Number	09/826,486
				Filing Date	April 4, 2001
				First Named Inventor	Ronald Foster
				Group Art Unit	2814
				Examiner Name	Not yet assigned
				Attorney Docket Number	4320.P045

U.S. PATENT DOCUMENTS

Examiner Initials *	U.S. Patent Document Number	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Filing Date if Appropriate
SA	4,589,028	Ochi	05-13-1986	
SA	5,198,880	Taguchi et al.	03-30-1993	
SA	5,574,789	Nakamura et al.	11-12-1996	
SA	5,841,126	Fossum et al.	11-24-1998	
SA	5,912,483	Hamada	06-15-1999	
SA	6,021,172	Fossum et al.	02-01-2000	
SA	6,028,773	Hundt	02-22-2000	
SA	6,104,059	Ono	08-15-2000	
SA	6,141,243	Aslam et al.	10-31-2000	

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Translation? Yes/No
	Office or Country	Number	Date			
SA	Europe	EP0942594A2		Canon Kabushiki Kasiha	9/15/1999	
SA	Germany	DE3733074A1	9/30/97	Computer Gesellschaft Konstanz		
SA	Japan	04112551		Rohm Co LTD	11/19/1993	

OTHER DOCUMENTS

Examiner Initials *	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published (if known).	Translation? Yes/No
SA	MURRAY, A.F. et al.; A User's Guide to Non-volatile, On-Chip Analogue Memory; Electronics & Communication Engineering Journal; April 1998	
SA	ANDERSON, S.; A Single Chip Sensor & Image Processor for Fingerprint Verification; Custom Integrated Circuits Conference; May 12-15, 1991; San Diego, CA, USA	
SA	ASLAM-SIDDIQI, AMER et al.; A 128-Pixel CMOS Image Sensor with Integrated Analog Nonvolatile Memory; IEEE Journal of Solid-State Circuits; Vol. 33; No. 10; October 1998	
SA	CALLIGARO, CRISTIANO et al.; A High-Speed Parallel Sensing Scheme for Multi-Level Non-Volatile Memories; IEEE, 1997	
SA	MIURA, HIROKI, A 100Frame/s CMOS Active Pixel Sensor for 3D-Gesture Recognition System; International Solid-State Circuits Conference; Feb. 16, 1999	

Examiner Signature	Date Considered
	1/30/04

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